## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Prior Application:

T. WADA et al

Serial No. 09/811,467 Filed: March 20, 2001

Group Art Unit:

2822

Examiner:

R. Potter

For:

A SEMICONDUCTOR DEVICE AND A METHOD

OF MANUFACTURING THE SAME

## INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

August 27, 2003

Sir:

In accordance with the duty of disclosure, the Applicants inform the Examiner of the documents cited during prosecution of the parent application, USSN 09/811,467.

The Applicants request the Examiner to initial and return a copy of the attached PTO-1449 form as an indication that the references have been considered.

Respectfully submitted,

Daniel J. Stanger

Registration No. 32,846 Attorney for Applicants

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Date: August 27, 2003

FORM PTO-1449 (REV. 7-80)			U.S. DEPART PATENT AND	MENT OF COMMERO TRADEMARK OFFI	CE ATTY. DOCKET NO. CE H-972-03	SERIA	SERIAL NO.			
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LIST OF DOCUMENTS CITED BY APPLICANT  T. WADA et al										
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				U.S. PATE	NT DOCUMENTS					
* EXAMINER INITIAL		DOCUMENT	DATE	NAME		CLASS	S SUBCLASS FILING DATE (If Appropriate			
	АА	6,285,084	09/2001	Hikita et al						
	АВ	6,265,763	07/2001	Jao et al						
	AC	5,724,233	03/1998	Honda et al						
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* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form next communication to applicant.										